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Version: 3.0

Date: September 23, 2005

Product Specification

15.0" XGA Color TFT-LCD Module Model Name: G150XG01 V.0

Final Specifications



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| Version and Date | Page | Old description | New Description | Remark |
|-------------------|-------|---|--|------------------------------------|
| 1.0 Feb. 6, 2004 | All | N/A | First Edition for Customers | |
| 2.0 Aug.16, 2004 | 8 | Anti-glare, hard coating (3H) | Anti-glare, hard coating (3H), low reflection | Surface treatment |
| Aug.16, 2004 | 13 | Min. 320 nits | Min. 300 nits | White Luminance @ CCFL 6.5mA |
| Aug.16, 2004 | 21 | | Lamp life: 50,000 hrs (typ.) Ambient temp 20 ~35 (with no wind) | |
| 3.0 Jul. 11, 2005 | 5 | N/A | This module comforming to RoHS directive is available starting from Q3 2005. Add shipping label figure | |
| | 6 | N/A | Weight 1150g (max) | |
| | 6 | Low reflection | Remove low reflection | |
| | 6, 12 | Contrast Ratio 400:1 (typ) | Contrast Ratio 500:1 (typ) | |
| | 8 | Operating Humidity 20~80% Storage Humidity 5~95% | Operating Humidity 8~90% Storage Humidity 8~90% Update wet bulb temperature chart | |
| | 10-12 | N/A | Add optical characteristic notes | |
| | 20 | Lamp life typical 50,000 hrs | Lamp life minumn 50,000 hrs | |
| | 24 | Non green design label sample | Green Design (RoHS) version label sample | |
| | | | | |
| | | | | |
| | | | | |



- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open nor modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the CCFL Reflector edge. Instead, press at the far ends of the CFL Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) After installation of the TFT Module into an enclosure (LCD monitor housing, for example), do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Cold cathode fluorescent lamp in LCD contains a small amount of mercury. Please follow local ordinances or regulations for disposal.
- 13) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (2.11, IEC60950 or UL1950), or be applied exemption.
- 14) The LCD module is designed so that the CFL in it is supplied by Limited Current Circuit (2.4, IEC60950 or UL1950). Do not connect the CFL in Hazardous Voltage Circuit.



2.0 General Description

This specification applies to the 15.0 inch Color TFT-LCD Module G150XG01.

The display supports the XGA (1024(H) x 768(V)) screen format and 262,144 colors (RGB 6-bits data).

All input signals are 1 Channel LVDS interface compatible.

This module does not contain an inverter card for backlight.

This module comforming to RoHS directive is available starting from Q3 2005.

Product Shipping Label (on the rear side of TFT-LCD module)



XXXXXXXXXXXX-XXXXX

Manufactured YY/MM Model No: G150XG01 AU Optronics MADE IN CHINA (S1)

H/W:XX F/W:X

Non Green Design



Manufactured YY/MM Model No: G150XG01 AU Optronics MADE IN CHINA (S1)

H/W:XX F/W:X

Green Design (RoHS directive)

Features

- -XGA 1024(H) x 768(V) resolution
- 50,000 hours lamp life
- -2 CCFLs (Cold cathode Fluorescent Lamp)
- -High contrast ratio, high aperture ratio
- -Wide viewing angle
- -High-speed response
- -Low power consumption

Application

Industrial Applications

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2.1 Display Characteristics

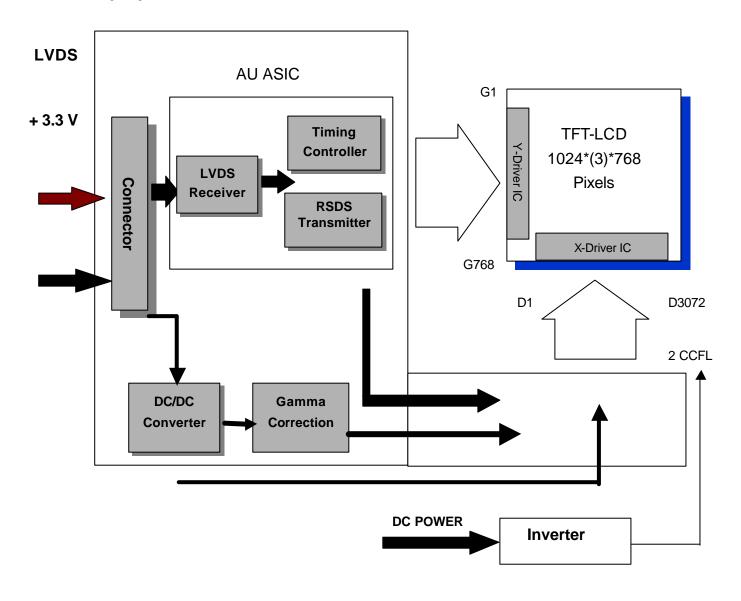
The following items are characteristics summary on the table under 25 condition:

| ITEMS | Unit | SPECIFICATIONS |
|---------------------------|----------------------|---|
| Screen diagonal | [mm] | 381 (15") |
| Outline dimension | [mm] | 326.5 (Typ.) x 253.5 (Typ.) x 12.0 (Max.) |
| Display Area | [mm] | 304.128 (H) x 228.096(38.1cm diagonal) |
| Resolution | | 1024(R,G,B x 3) x 768 |
| Pixel Pitch | [mm] | 0.297 x 0.297 |
| Pixel Arrangement | | R.G.B. Vertical Stripe |
| Display Mode | | TN mode, Normally White |
| Typical white Luminance. | [cd/m ²] | 350nit (Typ.) @ 6.5mA |
| Brightness uniformity | | 80% (Typ.) (Note 2) |
| Luminance uniformity | | 1.7 (Max.) (Note 3) |
| Crosstalk (at 60Hz) | | 1.2% (Max.) (Note 4) |
| Contrast Ratio | | 500 : 1 (Typ.) |
| Support Colors | | 262,144 colors (6-bit for R,G,B) |
| Chromaticity(CIE1931) | | |
| White-x | | 0.313 |
| | | 0.329 |
| White-y | | |
| Color Gamut | | 60% (Typ.) of NTSC coverage |
| Viewing angle (L/R/U/D) | | 60/60/40/60 (CR 10) |
| Response Time | [msec] | 16ms (Typ.) (Tr +Tf) |
| Nominal Input Voltage VDD | [Volt] | +3.3 V |
| Power Consumption | [Watt] | 11.5 W (Typ.) @6.5mA (All Black Pattern) |
| (VDD line + CCFL line) | | |
| Electrical Interface | | LVDS (1 Ch) |
| Frame rate | [Hz] | 60Hz (Typ.), 75Hz (Max.) |
| Weight | [Grams] | 1100 (Typ.) 1150 (Max.) |
| Mounting method | | Side mounting |
| Surface treatment | | Anti-glare, hard coating (3H) |
| Temperature Range | | |
| Operating | [°C] | 0 to +50 |
| Storage (Shipping) | [°C] | -20 to +60 |



2.2 Functional Block Diagram

The following diagram shows the functional block of 15.0 inches Color TFT-LCD Module:



DF-14H-20P-1.25H (Hirose) CWY20G-A0D1T (PTWO) JST BHR-03VS-1



3.0 Absolute Maximum Ratings

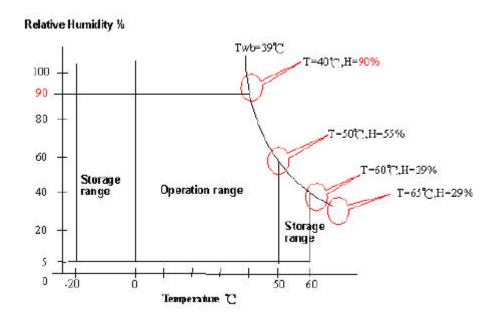
Absolute maximum ratings of the module is as following:

| Item | Symbol | Min | Max | Unit | Conditions |
|-------------------------|--------|------|--------------|----------|----------------|
| Logic/LCD Drive Voltage | VDD | -0.3 | +3.6 | [Volt] | |
| Input Voltage of Signal | Vin | -0.3 | +3.6 | [Volt] | |
| CCFL Current | ICFL | 2.5 | 8.5 | [mA] rms | Note 1 |
| Operating Temperature | TOP | 0 | +50 | [°C] | Note 2 |
| Operating Humidity | HOP | 8 | 90 | [%RH] | Note 2 |
| Storage Temperature | TST | -20 | +60 | [°C] | Note 2 |
| Storage Humidity | HST | 8 | 90 | [%RH] | Note 2 |
| Vibration | | - | 1.5 / 10-200 | [G / Hz] | |
| Shock | | - | 50/20 | [G / ms] | Half sine wave |

Note 1: CCFL life time will drop dramatically if exceeding 6.5 mA; It will have safety concern if exceeding 8.5 mA.

Note 2: Maximum Wet-Bulb should be 39 and No condensation.

Note 3: For quality performance, please refer to AUO IIS (Incoming Inspection Standard). Please refer to the graph below for corresponding of temperature and humidity.



Wet bulb temperature chart

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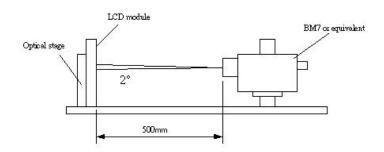
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4.0 Optical Characteristics

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 . The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of and equal to 0.°



| Item | Unit | Cond | itions | Min. | Тур. | Max. | Note |
|-------------------------------|----------------------|---------------------------------|-----------|-------|-------|-------|------|
| | | Horizontal | (Right) | 50 | 60 | | 1 |
| Viewing Angle | [degree] | CR 10 | (Left) | 50 | 60 | | |
| | [g] | | (Right) | 60 | 75 | - | |
| | | CR 5 | (Left) | 60 | 75 | | |
| | | Vertical | (Upper) | 30 | 40 | | |
| | [degree] | CR 10 | (Lower) | 50 | 60 | _ | |
| | | | (Right) | 45 | 50 | | |
| | | CR 5 | (Left) | 65 | 75 | | |
| Contrast ratio | | Normal Direction | | 400 | 500 | - | 2 |
| Response Time(Note 1) | [msec] | Raising Time Ton (10%-90%) | | - | 4 | 6 | 3 |
| | [msec] | Falling Time Toff | | - | 12 | 17 | |
| | [msec] | Raising | + Falling | - | 16 | 23 | |
| Color / Chromaticity | | Red x | | 0.587 | 0.617 | 0.647 | |
| Coordinates (CIE) | | Re | d y | 0.317 | 0.347 | 0.377 | |
| | | Gre | en x | 0.260 | 0.290 | 0.320 | |
| | | Gre | en y | 0.563 | 0.593 | 0.623 | |
| | | Blu | e x | 0.118 | 0.148 | 0.178 | |
| | | Blu | е у | 0.070 | 0.100 | 0.130 | |
| Color Coordinates (CIE) White | | White x | | 0.283 | 0.313 | 0.343 | |
| | | Whi | ite y | 0.299 | 0.329 | 0.359 | |
| Brightness Uniformity | [%] | | | 75 | 80 | - | 4 |
| White Luminance @ CCFL 6.5mA | [cd/m ²] | | | 300 | 350 | - | 6 |

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Note 1. Viewing Angle:

Viewing angle is the measurement of contrast ratio, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° () horizontal left and right and 90° () vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.

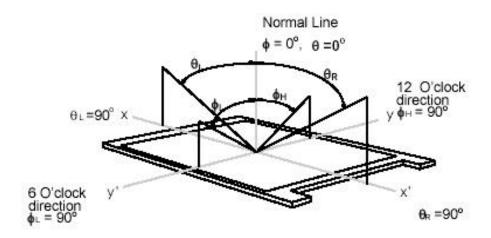
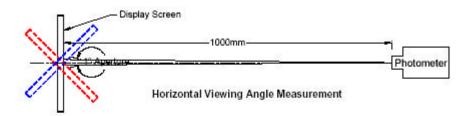
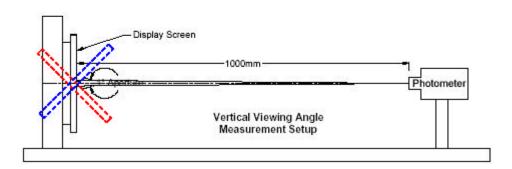


Fig.4-4 Viewing Angle Definition



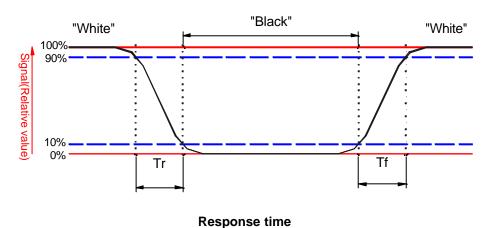




Note 2: Contrast Ratio (CR) is defined mathematically as:

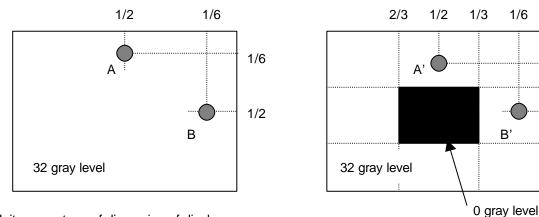
Note 3: Definition of Response time:

The output signals of photodetector are measured when the input signals are changed from "Black" to "White" (falling time), and from "White" to "Black" (rising time), respectively. The response time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 4: Brightness uniformity of these 9 points is defined as below: (Min. brightness / Max. brightness) x 100%

Note 5: Crosstalk is defined as below:



Unit: percentage of dimension of display area

I L_A - L_A · I / L_A x 100%= 1.2% max., L_A and L_B are brightness at location A and B (C) Copyright AU Optronics, Inc. G150XG01 V.0

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1/6 1/3

1/2

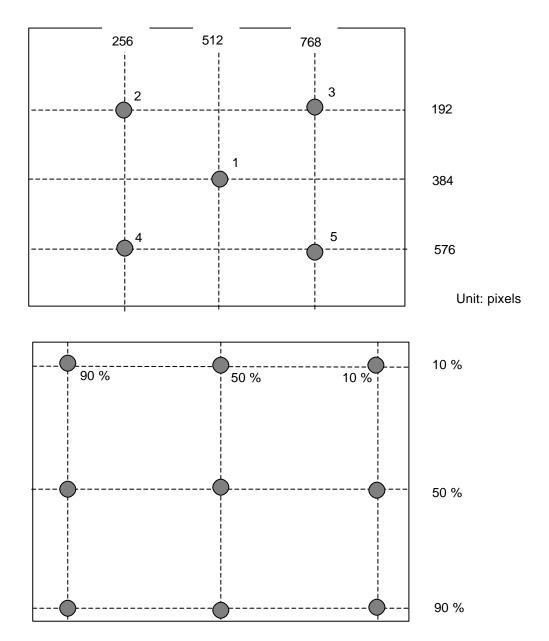
2/3



I $L_{B^{*}}L_{B^{'}}$ I / L_{B} x 100%= 1.2% max., $L_{A^{'}}$ and $L_{B^{'}}$ are brightness at location $A^{'}$ and $B^{'}$



Note 6: Brightness is measured at the center point of brightness value with all pixels displaying white.



Unit: percentage of dimension of display area



5.0 Signal Interface

5.1 Module Interface Connectors

Physical interface is described as for the connector on module.

These connectors are capable of accommodating the following signals and will be following components

| Connector Name | Interface Connector |
|--------------------|--|
| Manufacturer | Hirose, PTWO or compatible |
| Type / Part Number | DF-14H-20P-1.25H (Hirose) CWY20G-A0D1T (PTWO) |

5.2 Module Connector Pin Configuration

| DF-14H-2 | 20P-1.25H (H | Hirose) or CWY20G-A0D1T (PTWO) |
|----------|--------------|--|
| Pin No. | Symbol | Description |
| 1 | VDD | Power Supply, 3.3V (typical) |
| 2 | VDD | Power Supply, 3.3V (typical) |
| 3 | VSS | Ground |
| 4 | VSS | Ground |
| 5 | Rin0- | - LVDS differential data input (R0-R5, G0) |
| 6 | Rin0+ | + LVDS differential data input (R0-R5, G0) |
| 7 | vss | Ground |
| 8 | Rin1- | - LVDS differential data input (G1-G5, B0-B1) |
| 9 | Rin1+ | + LVDS differential data input (G1-G5, B0-B1) |
| 10 | vss | Ground |
| 11 | Rin2- | - LVDS differential data input (B2-B5, HS, VS, DE) |
| 12 | Rin2+ | + LVDS differential data input (B2-B5, HS, VS, DE) |
| 13 | VSS | Ground |
| 14 | CIkIN- | - LVDS differential clock input |
| 15 | ClkIN+ | + LVDS differential clock input |
| 16 | vss | Ground |
| 17 | Rin3- | NC |
| 18 | Rin3+ | NC |
| 19 | VSS | Ground |
| 20 | NC | Please *floating* and don't connect to ground. |



5.3 Backlight Connectors

| Connector Name / Designation | For Lamp Connector | | |
|------------------------------|---------------------|--|--|
| Manufacturer | JST or competiable | | |
| Type / Part Number | BHR-03VS-1 | | |
| Mating Type / Part Number | SM02(8.0)B-BHS-1-TB | | |

5.4 Backlight Connector Pin Configuration

| Pin | Symbol | Description |
|-----|--------|-------------------|
| 1 | HV | Lamp High Voltage |
| 2 | NC | No connection |
| 3 | LV | Ground |

- ◆ Cable length: 140 +- 5 mm
- ◆ Connector-output position: right side (front view)
- Lamp assembly design shall be easy for replacement and repair.

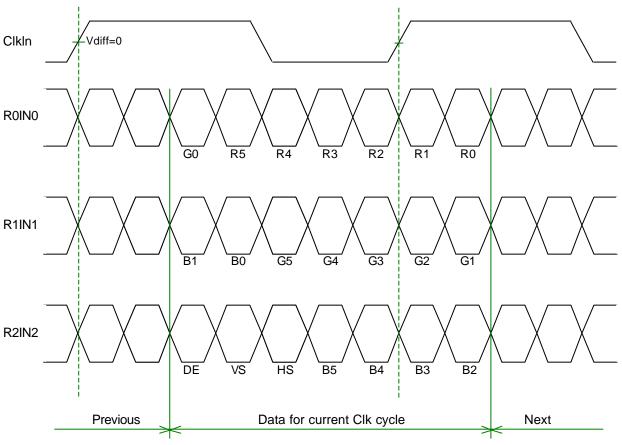
5.5 Signal Electrical Characteristics

Each signal characteristics are as follows;

| Item | Symbol | Min | Тур | Max | Unit |
|-----------------------------|--------|------|------|------|------|
| LCD Drive voltage | VDD | +3.0 | +3.3 | +3.6 | [V] |
| "High" input signal voltage | Vih | 2.0 | - | - | [V] |
| "Low" input signal voltage | Vil | - | - | 0.8 | [V] |



6-bits input data format



Note: R/G/B data 5: MSB, R/G/B data 0: LSB

Input signals shall be low or Hi-Z state when Vin is off It is recommended to refer to the specifications of SN75LVDS82DGG (Texas Instruments) in detail.

Each signal characteristics are as follows:

| Parameter | Condition | Min | Max | Unit |
|-----------|--|------|------|------|
| Vth | Differential InputHigh Voltage(Vcm=+1.2V) | - | +100 | [mV] |
| VtI | Differential Input Low Voltage(Vcm=+1.2V) | -100 | - | [mV] |

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5.6 Interface Timing Characteristics

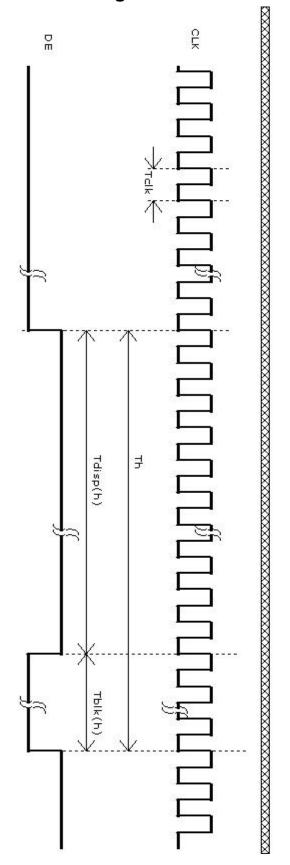
Basically, interface timings described here is not actual input timing of LCD module but output timing of SN75LVDS82DGG (Texas Instruments) or equivalent.

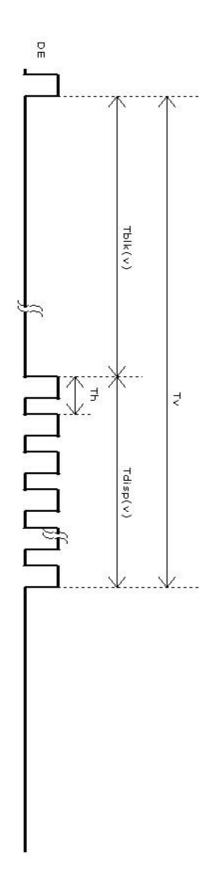
| Signal | Parameter | Symbol | MIN | TYP | MAX | Unit |
|--------------|---------------------|--------|------|------|------|------|
| Clock Timing | Clock frequency | clk | 50 | 65 | 81 | MHz |
| Hsync Timing | Horizontal active | Thd | 1024 | 1024 | 1024 | Tclk |
| | Horizontal blanking | Thbl | 30 | 320 | 1024 | Tclk |
| | Horizontal period | Th | 1054 | 1344 | 2048 | Tck |
| Vsync Timing | Vertical active | Tvd | 768 | 768 | 768 | Th |
| | Vertical blanking | Tvbl | 8 | 38 | 256 | Th |
| | Vertical period | Tv | 776 | 806 | 1024 | Th |

Note: Typical value refer to VESA STANDARD



5.7 Interface Timing Definition





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6.0 Pixel format imageFollowing figure shows the relationship of the input signals and LCD pixel format.

| | | 1 | | | 2 | | | | 1 02 | 23 | | 102 | 24 |
|------------|---|---|---|---|---|----|---|---|------|----|---|-----|----|
| 1 st Line | R | G | В | R | G | В. | 2.5.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2 | R | G | В | R | G | В |
| | | | | | | | | | | | | | |
| 768th Line | R | G | В | R | G | В | | R | G | В | R | G | В |

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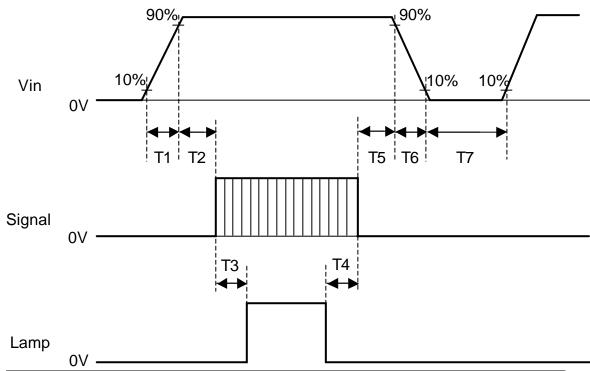
7.0 Power Consumption

Input power specifications are as follows;

| Symbol | Parameter | Min | Тур | Max | Unit | Condition |
|--------|--|-----|-----|-----|-------------|-----------------------------|
| VDD | LCD Drive Voltage | 3.0 | 3.3 | 3.6 | [V] | |
| IDD | LCD Drive Current | - | 1.0 | 1.3 | [A] | VDD=3.3V, All Black Pattern |
| PDD | LCD Drive power consumption | - | 3.3 | 4.3 | [Watt] | VDD=3.3V, All Black Pattern |
| VDDns | Allowable LCD Drive Ripple Noise | - | - | 100 | [mV] p-p | |

8.0 Power ON/OFF Sequence

Vin and lamp power on/off sequence are as follows. The timing of interface signal are shown in the table below.



| Symbol | | Unit | | |
|--------|------|------|-----|------|
| Symbol | Min | Тур | Max | Onit |
| T1 | 0.5 | - | 10 | [ms] |
| T2 | 0.5 | 25 | 50 | [ms] |
| T3 | 200 | - | - | [ms] |
| T4 | 200 | - | - | [ms] |
| T5 | 0.5 | 16 | 50 | [ms] |
| T6 | 0.5 | - | 10 | [ms] |
| T7 | 1000 | - | - | [ms] |

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9.0 Backlight Characteristics

9.1 Signal for Lamp connector

| Pin # | signal Name | | |
|-------|-------------------|--|--|
| 1 | Lamp High Voltage | | |
| 2 | No connection | | |
| 3 | Ground | | |

9.2 Parameter guide line for CCFL Inverter

| Symble | Parameter | Min | Тур | Max | Units | Condition |
|-------------------------------|---|--------|-----------------|-----------------|---------------|--|
| IRCFL | CCFL operation range | 2.5 | 6.5 | 8.5 | [mA] rms | (Ta=25) Note 4 |
| ICFL | CCFL Inrush current | - | - | 20 | [mA] | |
| FCFL | CCFL Frequency | 40 | 55 | 80 | [KHz] | (Ta=25) Note 1 |
| ViCFL (25) (reference) | CCFL Ignition Voltage | 1100 | - | - | [Volt] rms | (Ta=25) Note 3 |
| ViCFL (0) (reference) | CCFL Ignition Voltage | 1450 | - | - | [Volt] rms | (Ta=0) Note 3 |
| VCFL | CCFL Discharge Voltage | - | 645 (@6.5mA) | 710 (@2.5mA) | [Volt] rms | (Ta=25) Note 2 |
| PCFL | CCFL Power consumption @ 8mA (excluding inverter) | - | 8.5 | 11 | [Watt] | (Ta=25) Note 2 |
| Lamp life | | 50,000 | - | - | [Hrs] | Ambient temp 20 ~35 (with no wind) Note 4 |

Note 1: CCFL Frequency should be carefully determined to avoid interference between inverter and TFT LCD

Note 2: Calculator value for reference (IRCFLxVCFLx2=PCFL)

Note 3: CCFL inverter should be able to give out a power that has a generating capacity of over 1450 voltage. Lamp units need 1450 voltage minimum for ignition

Note 4: CCFL life time 50,000hr at 6.5mA. It's defined as the brightness of A point of lamp becomes 50% of the initial brightness. It's recommended not to exceed 6.5 mA for CCFL life time concern and it's prohibited to exceed 8.5 mA for safety concern.

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10.0 Vibration, Shock, and Drop

10.1 Vibration & Shock

The module shall work error free after following vibration and shock condition. Likewise the module shall not sustain any damage after vibration and shock test.

10.1.1 Vibration Test Spec:

• Frequency: 10 - 200Hz

Sweep: 30 Minutes each Axis (X, Y, Z)

Acceleration: 1.5G(10~200Hz P- P)

Test method:

| Acceleration (G) | 1.5 |
|-------------------|-----------|
| Frequency (Hz) | 10~200~10 |
| Active time (min) | 30 |

10.1.2 Shock Test Spec:

| Acceleration (G) | 50 |
|------------------|----------|
| Active time(ms) | 20 |
| Wave form | Half-sin |
| Times | 1 |

Direction: ±X, ±Y, ±Z

10.2 Drop

Package test: The drop height is defined as 60 cm.

(1 corner,3 edges,6 flat faces)
-- No damage and defect found for panel.



11.0 Environment

The display module will meet the provision of this specification during operating condition or after storage or shipment condition specified below. Operation at 10% beyond the specified range will not cause physical damage to the unit.

11.1 Temperature and Humidity

11.1.1 Operating Conditions

The display module operates error free, when operated under the following conditions;

Temperature 0 °C to 50 °C 20% to 85% Relative Humidity 39.0 °C Wet Bulb Temperature

11.1.2 Shipping Conditions

The display module operates error free, after the following conditions;

-20 °C to 60 °C Temperature 5% to 95% Relative Humidity 39.0 °C Wet Bulb Temperature

11.2 Atmospheric Pressure

The display assembly is capable of being operated without affecting its operations over the pressure range as following specified:

| | Pressure | Altitude |
|------------------|----------|----------------------|
| Maximum Pressure | 1040 hPa | 0 m = sea level |
| Minimum Pressure | 601 hPa | 3658 m = 12,000 feet |

Note: Non-operation altitude limit of this display module = 40,000 feet. = 12193 m.

11.3 Thermal Shock

The display module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20°C to 60°C, and back again.

-20 °C for 30min Thermal shock cycle 60 °C for 30min

Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before powering on.

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This display module and the packaging of that will comply following standards.

12.1 Failure Criteria

The display assembly will be considered as failing unit when it no longer meets any of the requirements stated in this specification. Only as for maximum white luminance, following criteria is applicable. Note: Maximum white Luminance shall be 75 cd/m² or more.

12.2 Failure Rate

The average failure rate of the display module (from first power-on cycle till 1,000 hours later) will not exceed 1.0%. The average failure rate of the display module from 1,000 hours until 10,000 hours will not exceed 0.70% per 1000 hours.

12.2.1 Usage

The assumed usage for the above criteria is:

- 220 power-on hours per month
- 500 power on/off cycles per month
- Maximum brightness setting
- Operation to be within office environment (25°C typical)

12.2.2 Component De-rating

All the components used in this device will be checked the load condition to meet the failure rate criteria.

12.3 CCFL Life

The assumed CCFL Life will be typically 50,000 hours under stable condition at 25 ±5 °C;

Standard current at 6.5 ± 0.5 mA

Definition of life: brightness becomes 50% or less than the minimum luminance value of CCFL.

12.4 ON/OFF Cycle

The display module will be capable of being operated over the following ON/OFF Cycles.

| ON/OFF | Value | Cycle |
|---------------------|--------|--------------------------------|
| +VDD and CCFL power | 36,000 | 10 seconds on / 10 seconds off |



13.1 Sharp Edge Requirements

There will be no sharp edges or corners on the display assembly that could cause injury.

13.2 Materials

13.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible AUO Toxicologist.

13.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process. The printed circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be printed on the printed circuit board.

14.0 Other requirements

14.1 Smoke Free Design

Any smoke nor strange smell shall not be observed by the operator at any single failure.

14.2 National Test Lab Requirement

The display module will satisfy all requirements for compliance to the following requirement:

UL 1950, First EditionU.S.A. Information Technology EquipmentCSA C22.2 No.950-M89Canada, Information Technology EquipmentIEC 950International, Information Technology EquipmentEN 60 950International, Information Processing Equipment

The construction of the display module is designed to suppress EMI. When mounted into a specified host system, the system will meet the following EMI requirement:

(European Norm for IEC950)

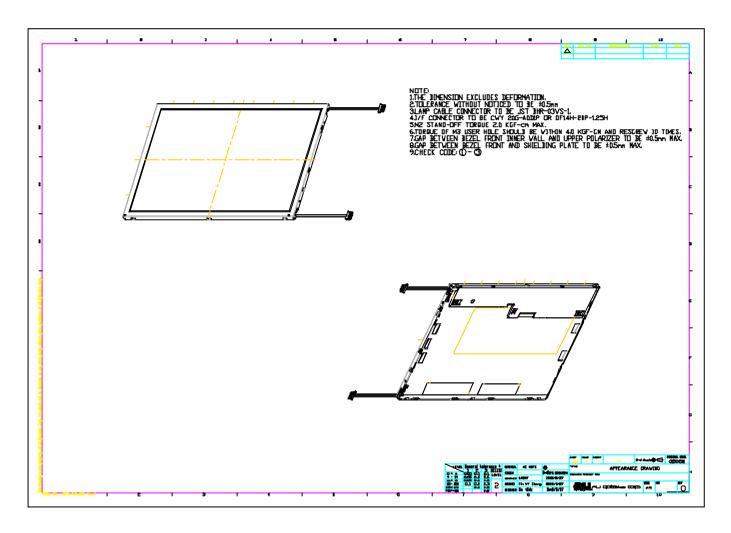
FCC Part 15 Class B VCCI Class 2 CISPR 22, class B Taiwan CNS standard China CCIB standard

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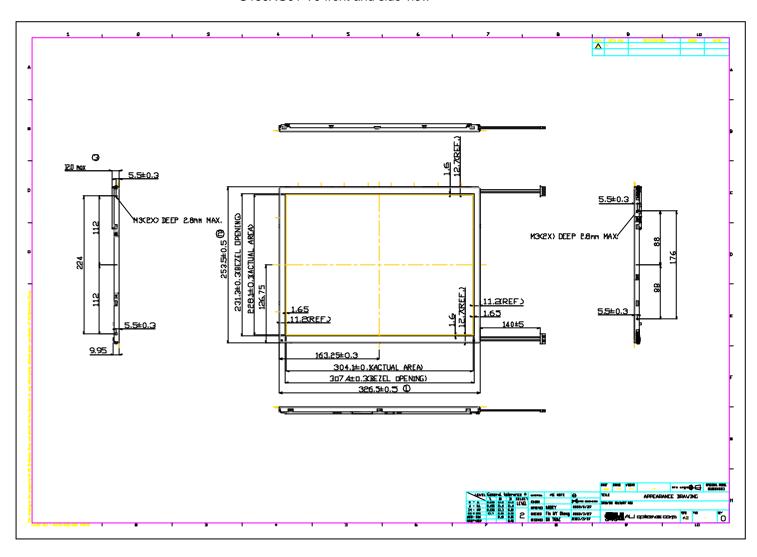
15.0 Mechanical Characteristics



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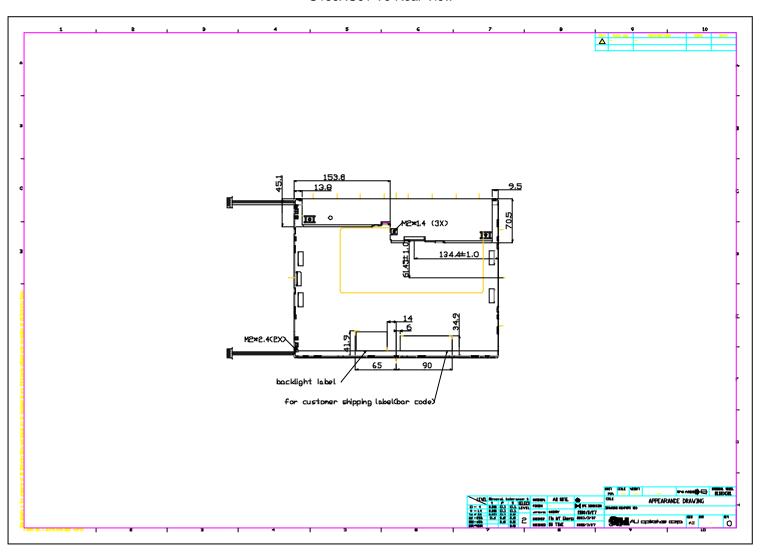
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